Quantum Error Correction

Admin

- November 27: No class
- December 2: No class
- December 4: Final review and Ask-me-anything, Pset 2 (Theory) due
- December 8: Last quiz 8 due (Practice final)
- December 9: In-class final
- December 15: Pset2 (Coding) due



- How to do deal with noise/errors in a quantum computation? A priori it seems very different from errors in classical computation (infinitely many possible errors on a single qubit, no-cloning, measurement collapse,...)
- Can reduce problem of error-correction for a single-qubit to a **discrete** set of errors
 - Bitflip error:

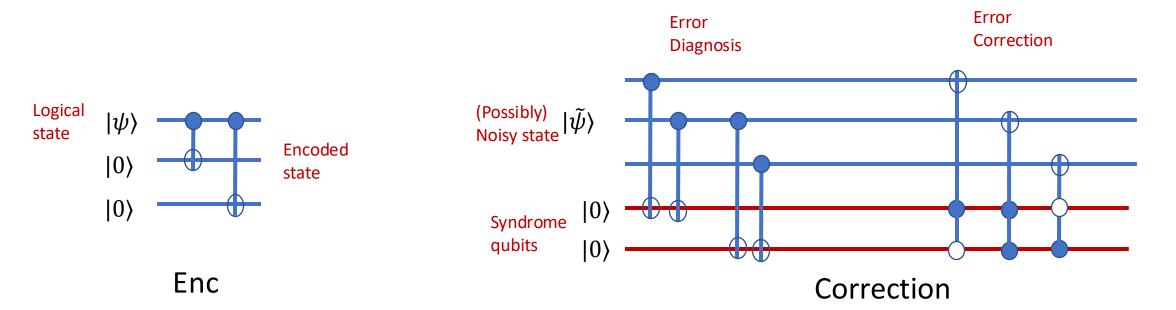
• Phaseflip error:

• Bitflip & Phaseflip error:

Bitflip code

 $\left|\overline{0}\right\rangle = \left|000\right\rangle$ $\left|\overline{1}\right\rangle = \left|111\right\rangle$

$$|\psi\rangle = \alpha|0\rangle + \beta|1\rangle \mapsto \alpha|000\rangle + \beta|111\rangle = |\overline{\psi}\rangle$$



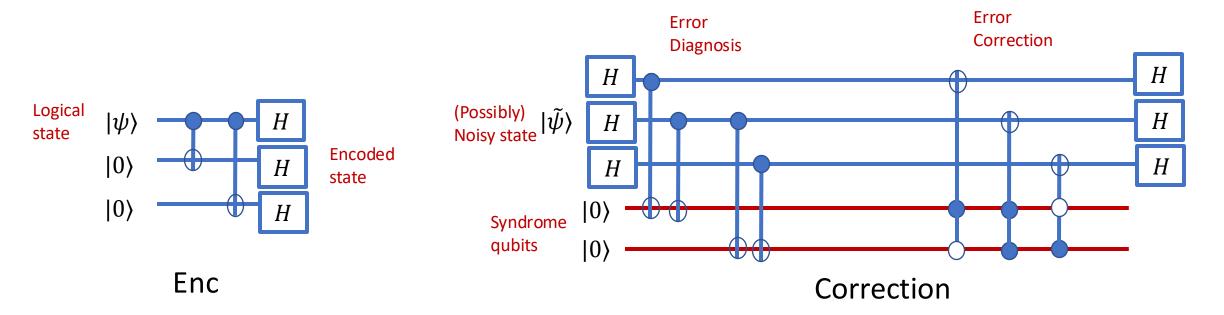
Phaseflip code

Key idea: phaseflip is just bitflip in diagonal basis!

$$\left|\overline{0}\right\rangle = \left|+++\right\rangle$$

 $\left|\overline{1}\right\rangle = \left|---\right\rangle$

$$|\psi\rangle = \alpha|0\rangle + \beta|1\rangle \mapsto \alpha|+++\rangle + \beta|---\rangle = |\overline{\psi}\rangle$$



Concatenate bitflip and phaseflip codes together:

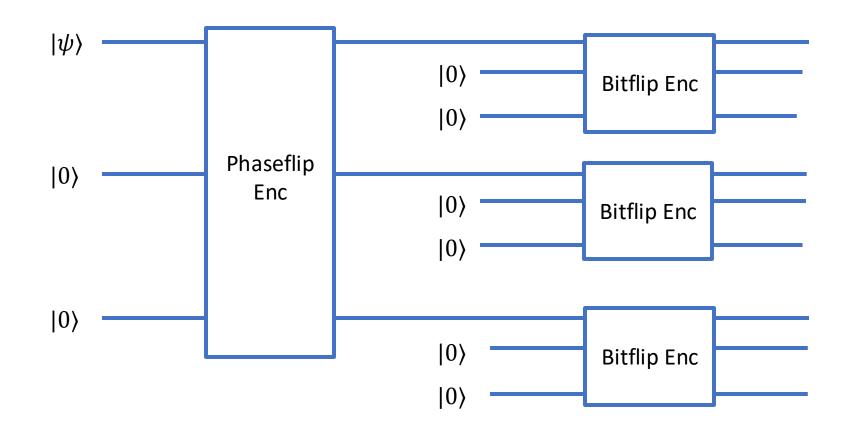
• First, encode logical qubit using 3-qubit phaseflip code. Then, encode each of those 3 qubits using bitflip code.

$$|0\rangle \mapsto |+\rangle^{\otimes 3} \mapsto \frac{1}{\sqrt{8}} (|000\rangle + |111\rangle)^{\otimes 3}$$

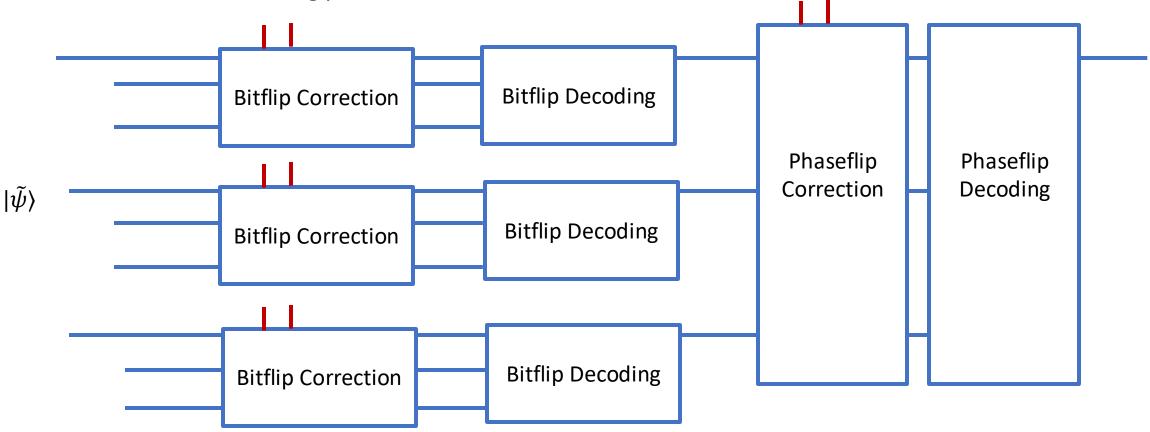
$$|1\rangle \mapsto |-\rangle^{\otimes 3} \mapsto \frac{1}{\sqrt{8}}(|000\rangle - |111\rangle)^{\otimes 3}$$

Claim: this code can correct both bitflip and phaseflip errors, so long as they occur on a single qubit.

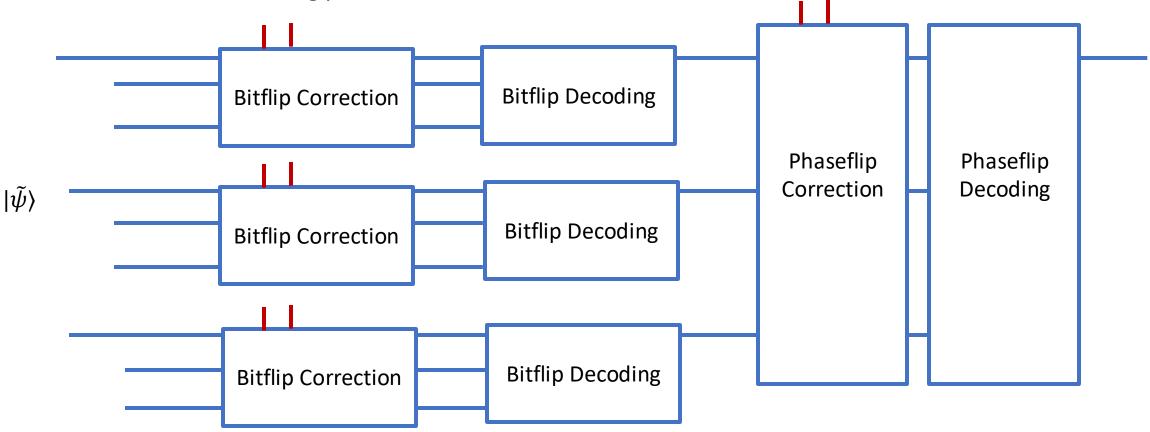
Encoding:

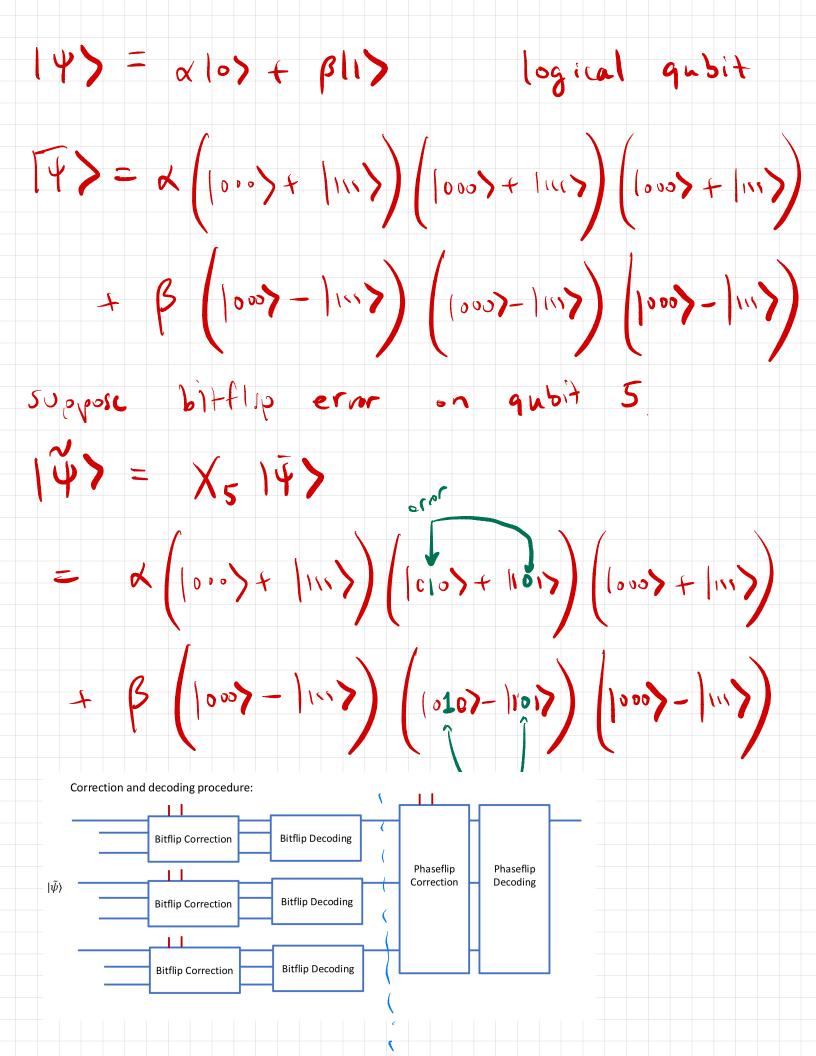


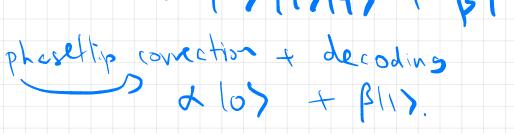
Correction and decoding procedure:



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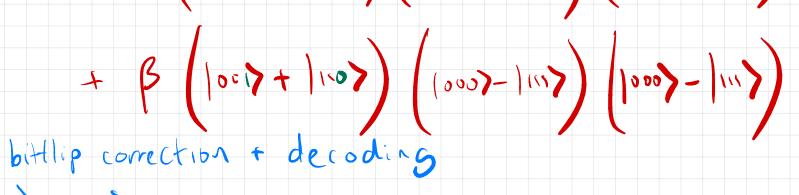


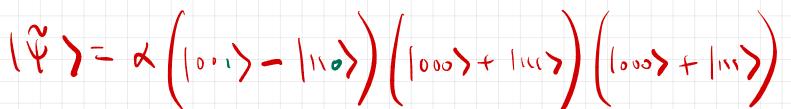


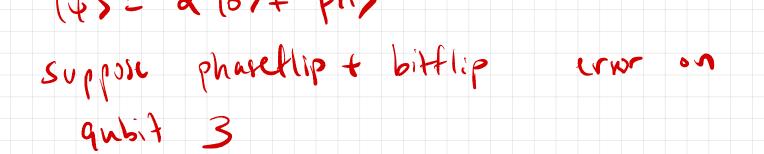


(4)= d lo7+ Bli>

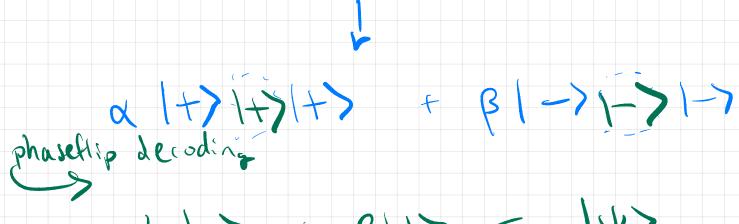
--> < 1-> [f>]f>]f>]f>] + B [+> 1-> 1->











Shor code can detect and fix:

- Any single qubit X error
- Any single qubit Z error
- Any single qubit XZ error

Why errors can be discretized $\gamma = iZX = \begin{pmatrix} \circ & i \\ -i & \circ \end{pmatrix}$

Every Unitary can be written as a linear combination of Pauli matrices:

$$= \{ I, X, Y, Z \}$$

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Why errors can be discretized

Every unitary can be written as a linear combination of Pauli matrices:

Let C be the Shor error correction circuit.

$$C|\Psi\rangle|_{0-\infty} = \alpha C|\Psi\rangle|_{0-\infty} + bC\chi_{i}|\Psi\rangle|_{0-\infty}$$

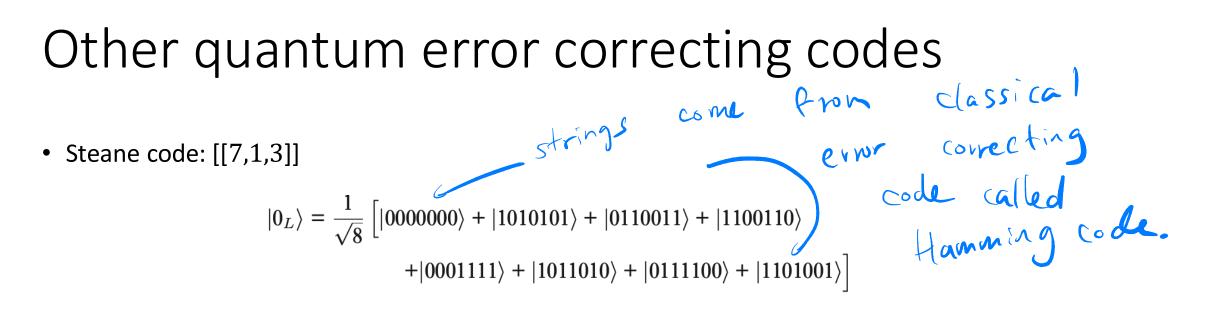
 $+ c CZ_{i}\chi_{i}|\Psi\rangle|_{0-\infty} + dCZ_{i}|\Psi\rangle|_{0-\infty}$
 $= \alpha |\Psi\rangle|_{e_{1}} + b|\Psi\rangle|_{e_{x}} + c|\Psi\rangle|_{e_{y}} + d|\Psi\rangle|_{e_{z}}$
 $= |\Psi\rangle \otimes (\alpha|_{e_{1}}) + b|e_{x}\rangle + c|e_{y}\rangle + d|e_{z}\rangle$

Why errors can be discretized

Can handle more than just unitary errors:

Other quantum error correcting codes

- Shor's code is a [[9,1,3]] code.
- A [[*n*, *k*, *d*]] quantum code encodes *k* → *n* qubits, and has distance *d*, meaning that there are two encoded states that are separated by a *d*-qubit error.
- Such a code can correct up to $\left\lceil \frac{d-1}{2} \right\rceil$ errors.
- Desiderata
 - Make *n* as small as possible
 - Make *k*, *d* large as possible.
 - Fast encoding/decoding

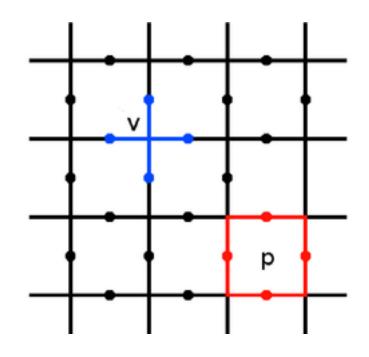


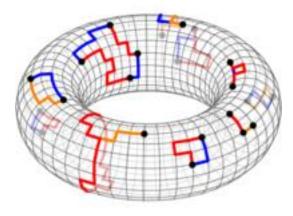
$$\begin{aligned} 1_L \rangle &= \frac{1}{\sqrt{8}} \left[|111111\rangle + |0101010\rangle + |1001100\rangle + |0011001\rangle \\ &+ |1110000\rangle + |0100101\rangle + |1000011\rangle + |0010110\rangle \right] \end{aligned}$$

- Laflamme, et al. [[5,1,3]] code
 - Smallest code that can correct single-qubit errors!

The Toric Code / surface code.

- Widely considered a front-runner for implementation.
- $[[n, 2, \sqrt{n}]]$ code. Quantum LDPC code, fast encoding/decoding, ...
- Qubits arranged on a 2D grid, which we can think of as living on a torus.
- An example of a *topological code*: Error-correction properties are determined by *topology* of torus.





Toric code

Fault tolerance

• Our analysis of Shor's code assumes that encoding/correction/decoding are all done perfectly.

• But in general all of these operations will experience errors. How do we cope with errors then?

 Also, we don't just want to store information – we want to compute with it! If we have encoded data, how to perform computations on them without decoding?

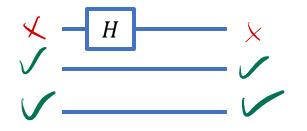
Fault tolerance

• Main idea: only decode information *only when absolutely necessary*. Perform computations *on top of* encoded data.

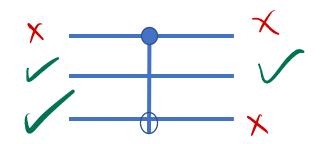
• Use quantum error correcting codes that allow for computations to be performed on encoded states, *without* requiring intermediate decoding.

• Encoded operations should propagate errors in a controlled fashion.

Error propagation



Single qubit gates do not spread errors



Two-qubit gates can spread errors

Performing encoded gates

An encoded gate (single-qubit) G for a quantum error-correcting code C is a unitary \overline{G} where

$$\overline{G} \left| \overline{\psi} \right\rangle = \overline{(G|\psi\rangle)}$$

Ideally, \overline{G} does not entail decoding, applying G "in the clear", and then re-encoding.

Many codes (such as Steane, Shor) allow for *transversal* encoded gates for a subset of gates. A single-qubit gate G is transversal for a code C if

$$G^{\otimes n} \left| \overline{\psi} \right\rangle = \overline{(G|\psi\rangle)}$$

Performing encoded gates



Implements $\overline{X|\psi}$.

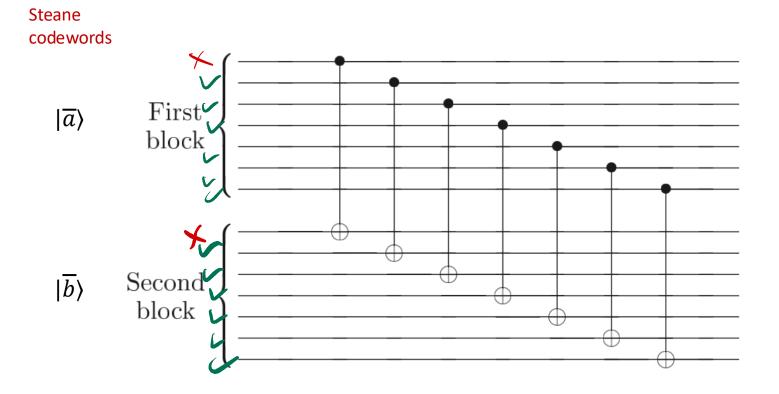


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Steane code supports transversal gates for any *Clifford* gate: X, Y, Z, P, H, CNOT.

Note: doesn't work exactly like this for Shor code

Performing encoded gates



Implements $\overline{CNOT|a,b}$.

Eastin-Knill Theorem

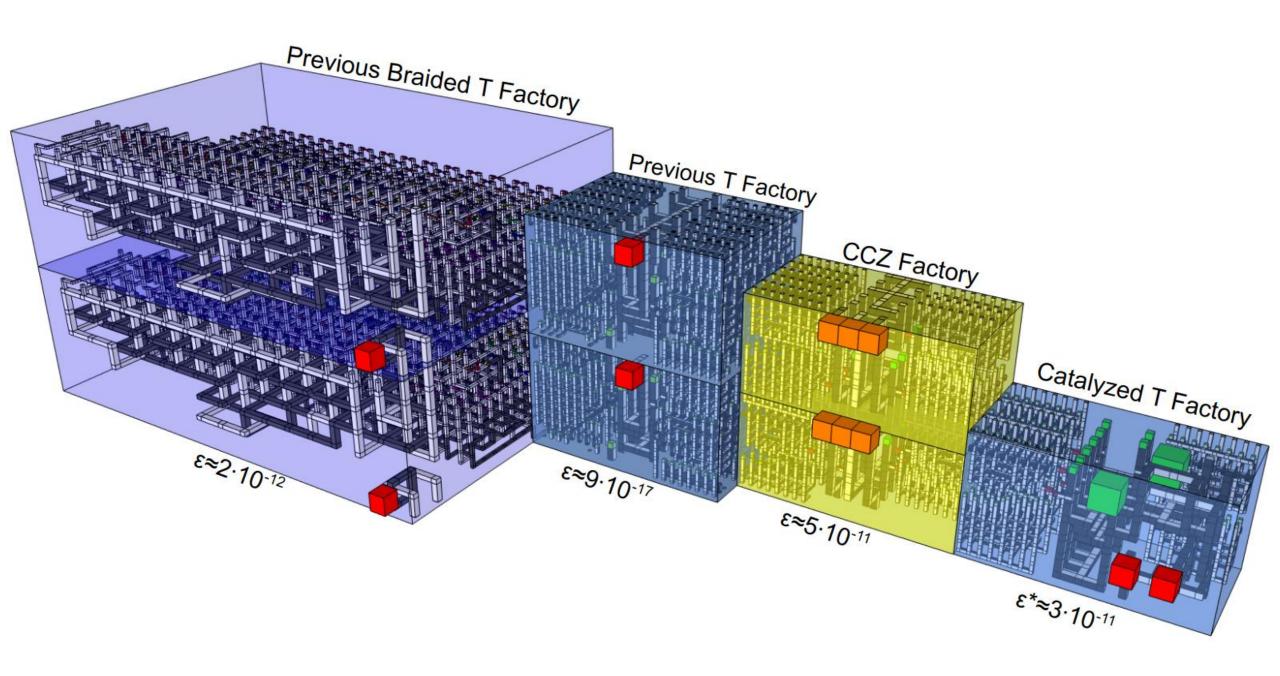
Life would be really nice if there was a code that supported transversal gates for a universal gate set.

Eastin-Knill Theorem: Nope.

For Steane code, what's missing is a transversal implementation of
$$T = \begin{pmatrix} 1 & 0 \\ e^{i\pi/4} \end{pmatrix}$$
 gate.

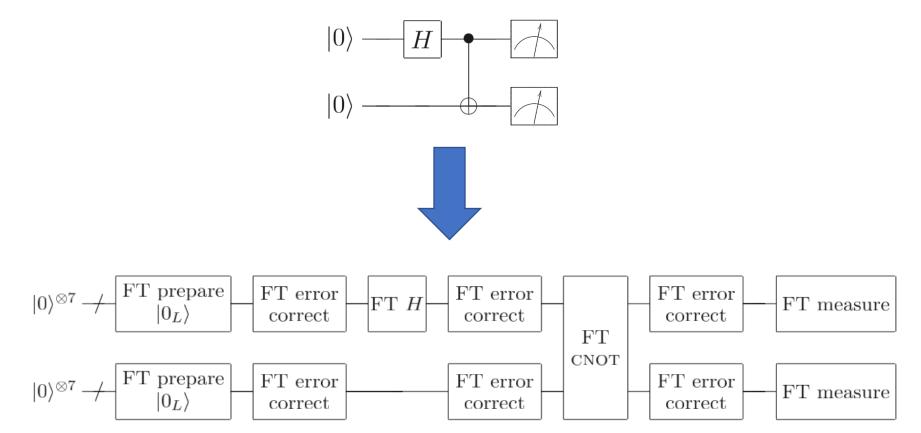
Need another way of performing encoded *T* gates. Many different solutions, all complex, and the main bottleneck for implementing large-scale fault tolerance.

e.g. "magic state distillation/injection", "magic state factories", "code switching", ...



Fault tolerance

In addition to performing encoded gates, periodically have to check for errors and correct them "mid-flight".



Fault-Tolerance Theorem (a.k.a. the *Threshold Theorem*)

a.k.a. the Theorem that billions of dollars of investment from companies and governments is dependent on.

(Aharonov, Ben-Or 1998, Zurek et al. 1996) For a wide class of noise models, there exists a universal threshold p^* , such that if the error of a single gate/qubit at any time is $p \le p^*$, then assuming:

- Parallel gates
- Intermediate measurements/ability to reset, discard qubits
- Extremely fast, reliable classical computation

Every quantum computation of size S can be converted to a fault-tolerant implementation of size $O(S \log S)$ that outputs the correct answer with high probability.

Today, people estimate $p^* \approx 10^{-4}$ for i.i.d. depolarizing noise.

Today, and the future

We do not have fault tolerant systems yet. People are experimenting with quantum error correction schemes on small scale quantum computers, and seeing promising results.

2023: Harvard/QuERA, Platform: neutral atoms trapped using optical tweezers

280 physical qubits \rightarrow 48 logical qubits

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https://www.quantamagazine.org/the-best-qubits-for-quantum-computing-might-just-be-atoms-20240325/

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2024: The Year of Quantum Error Correction experiments

August (Quantinuum + Microsoft): 32 physical qubits \rightarrow 4 logical qubits, trapped ions platform August (Google): 101 physical qubits \rightarrow distance 5 and distance 7 codes, superconducting qubits November (Atom + Microsoft): 256 physical qubits \rightarrow 24 logical qubits, neutral atoms

Today, and the future

- We do not have fault tolerant systems yet. People are experimenting with quantum error correction schemes on small scale quantum computers, and seeing promising results.
- Three main directions:
 - Building better qubits/gates
 - Scaling up
 - Better error-correction/fault-tolerance schemes